

1 1 8  
Eischwind et al  
1089-2001-DLWDL (RPT) (8728-545)

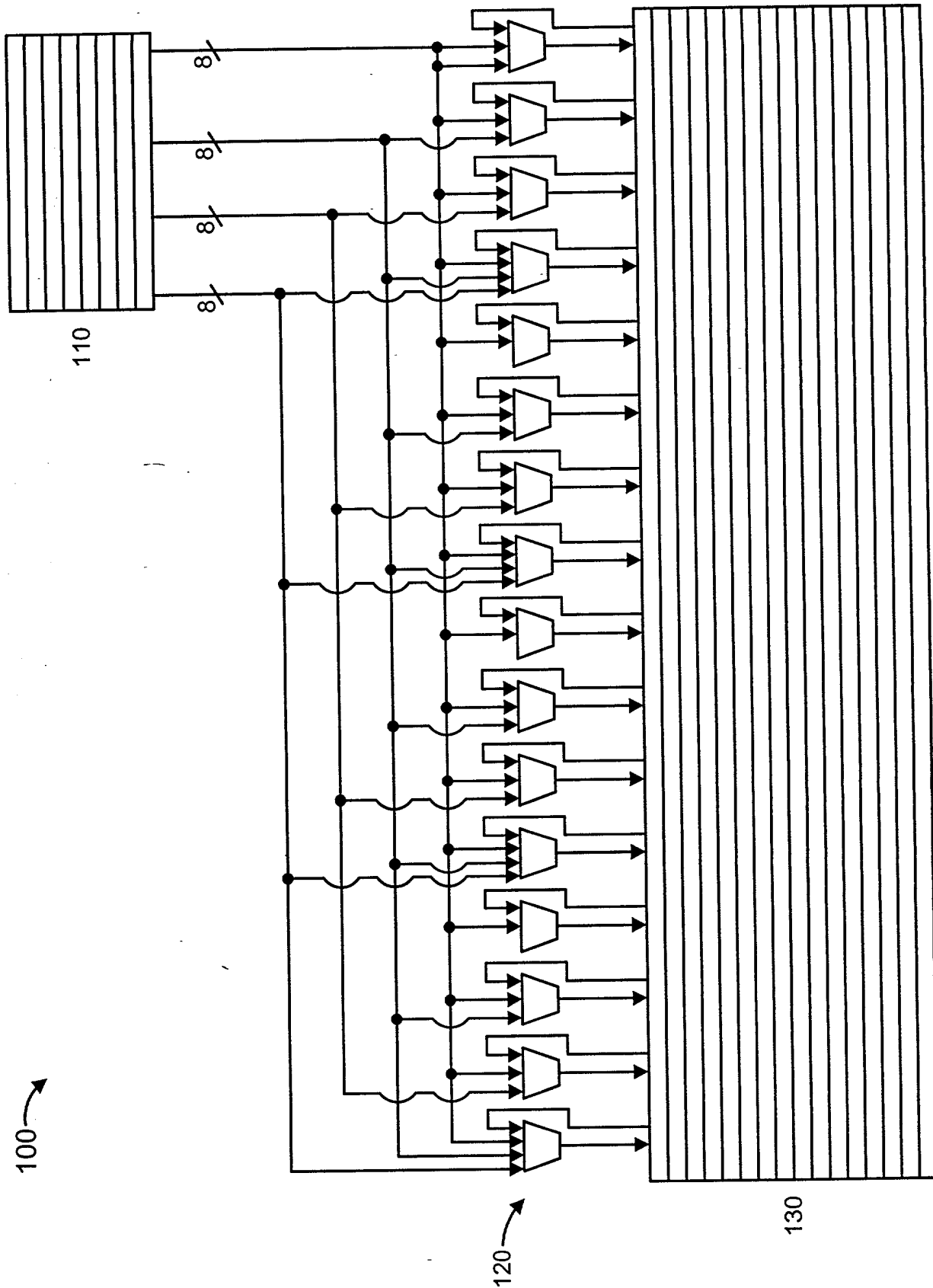


FIG. 1  
(PRIOR ART)



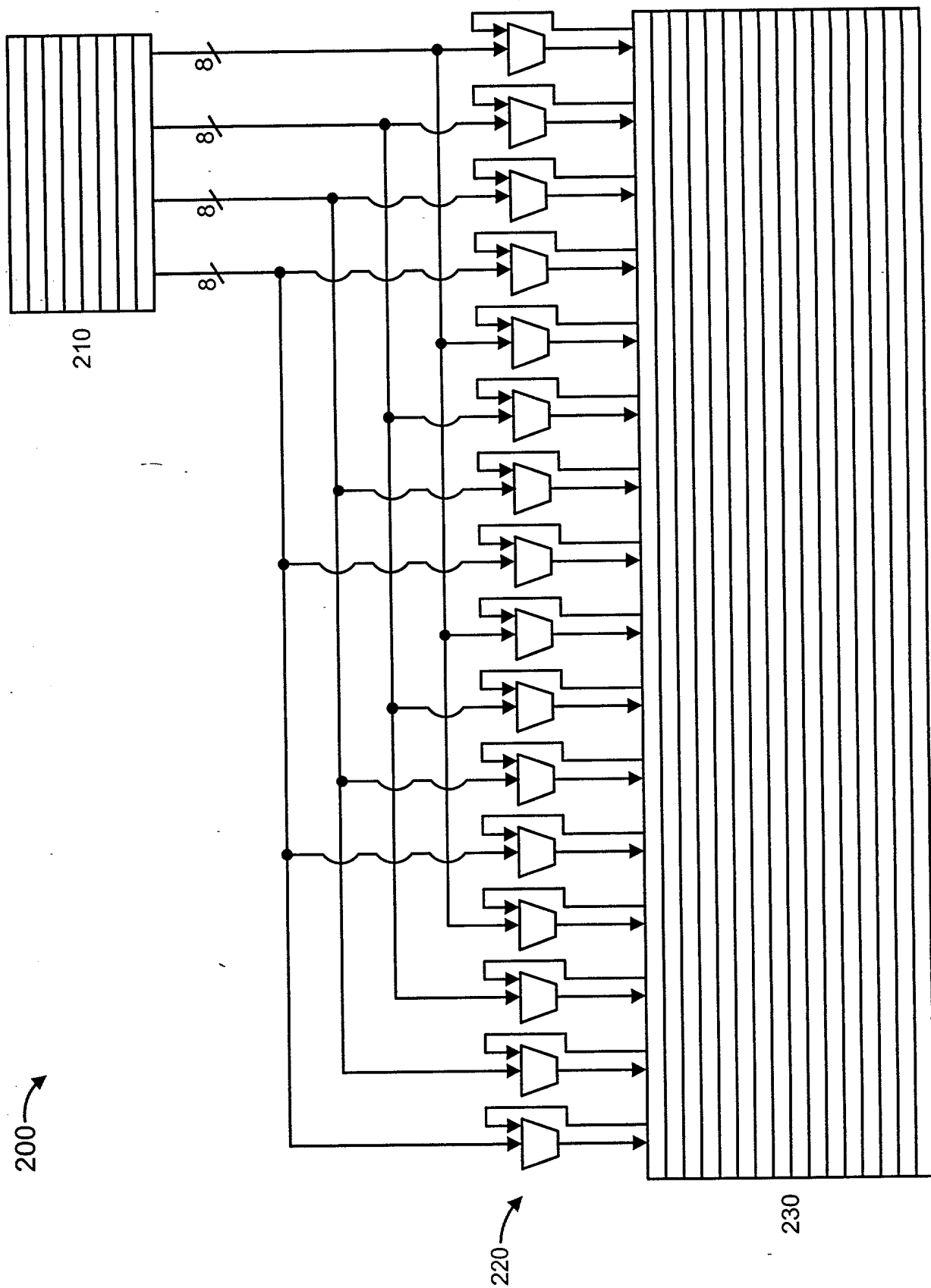


FIG. 2  
(PRIOR ART)



3 / 8  
YDR9-2001-0606 (8728-545)

300

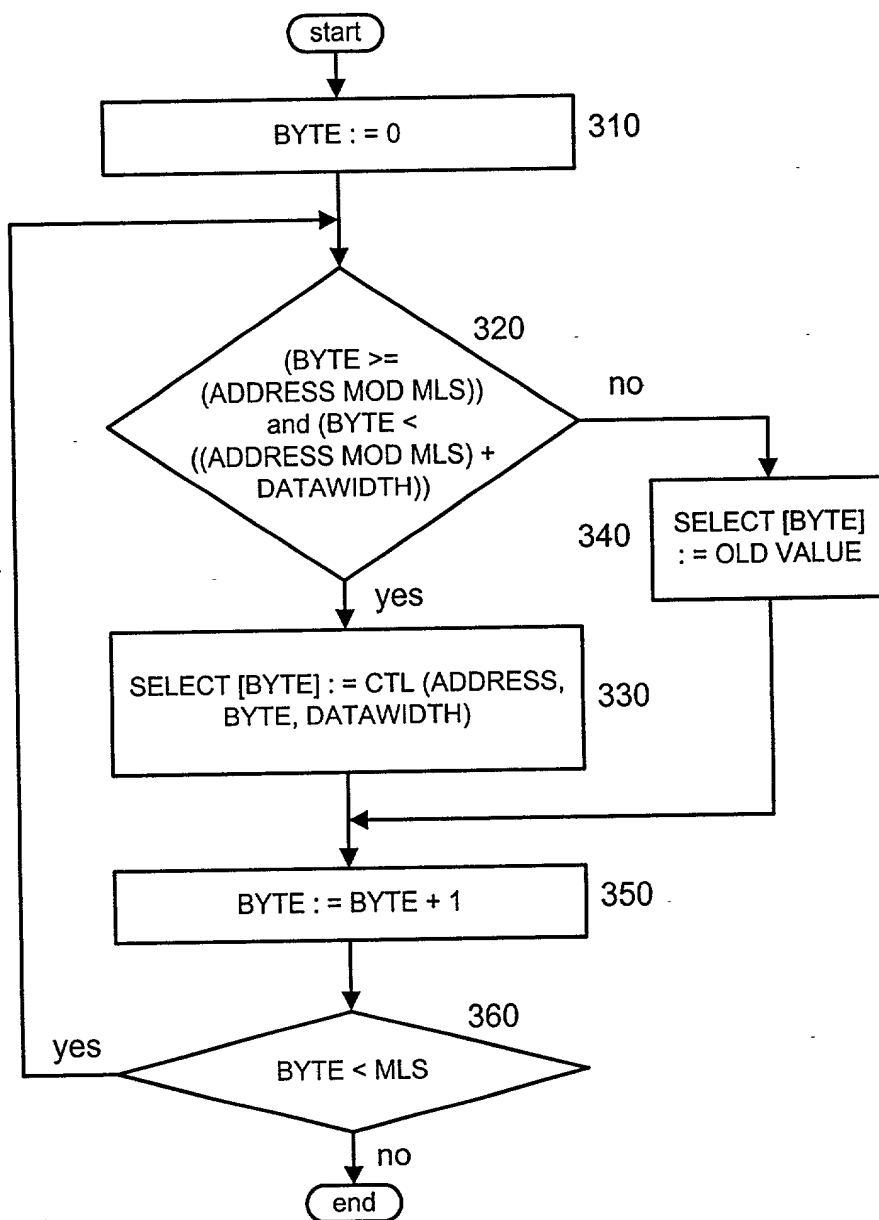


FIG. 3  
(PRIOR ART)



4 / 8  
YDR9-2001-0606 L8728-5451

400

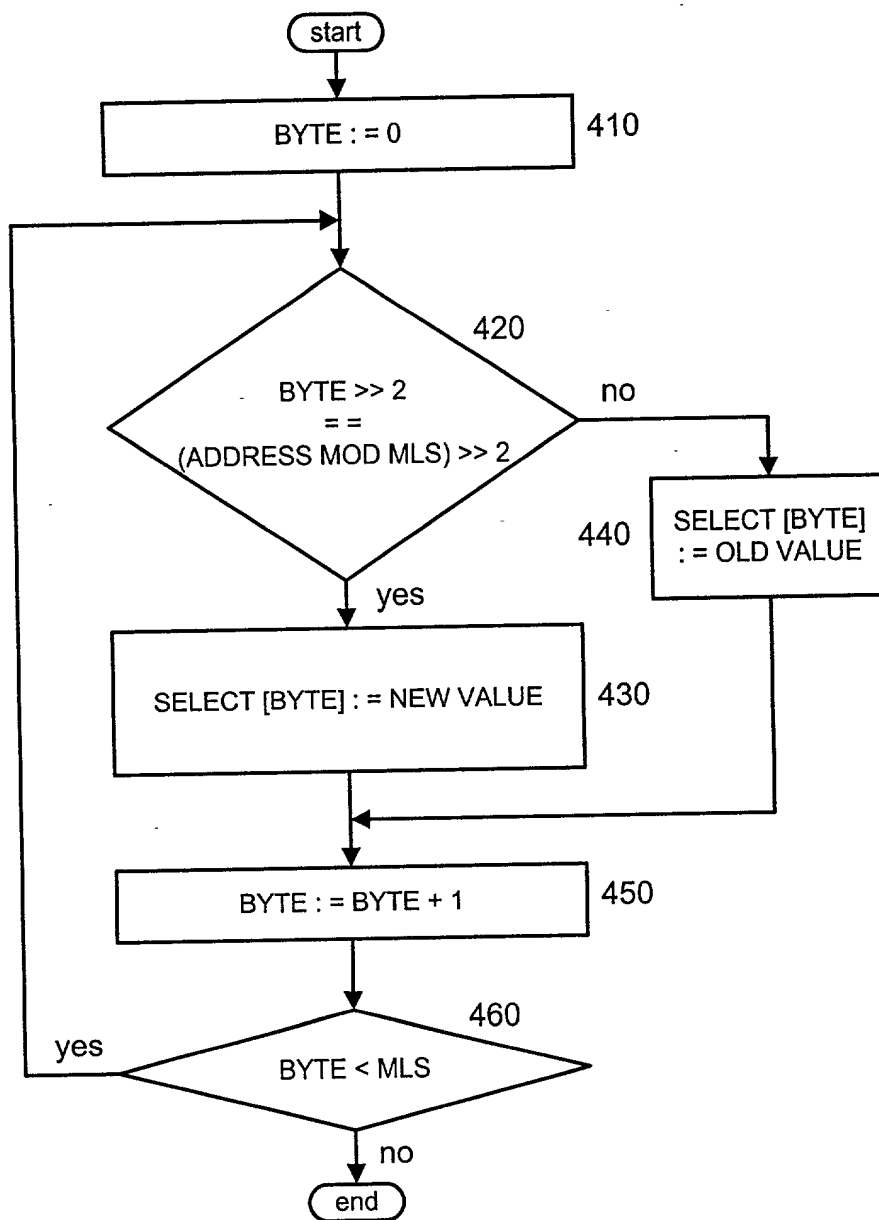


FIG. 4  
(PRIOR ART)



500

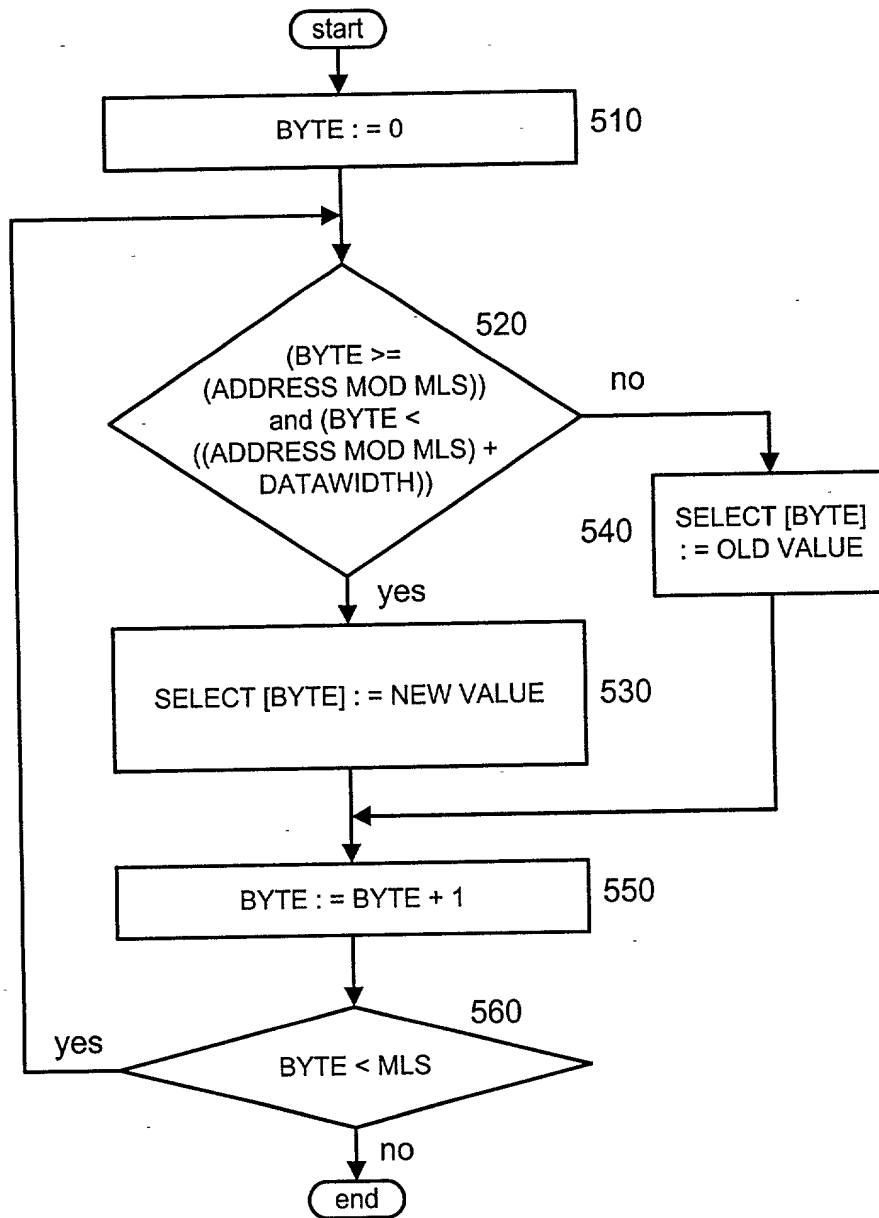


FIG. 5



6/8  
VDRG-2001-D606 (8728-545)

FIG. 6

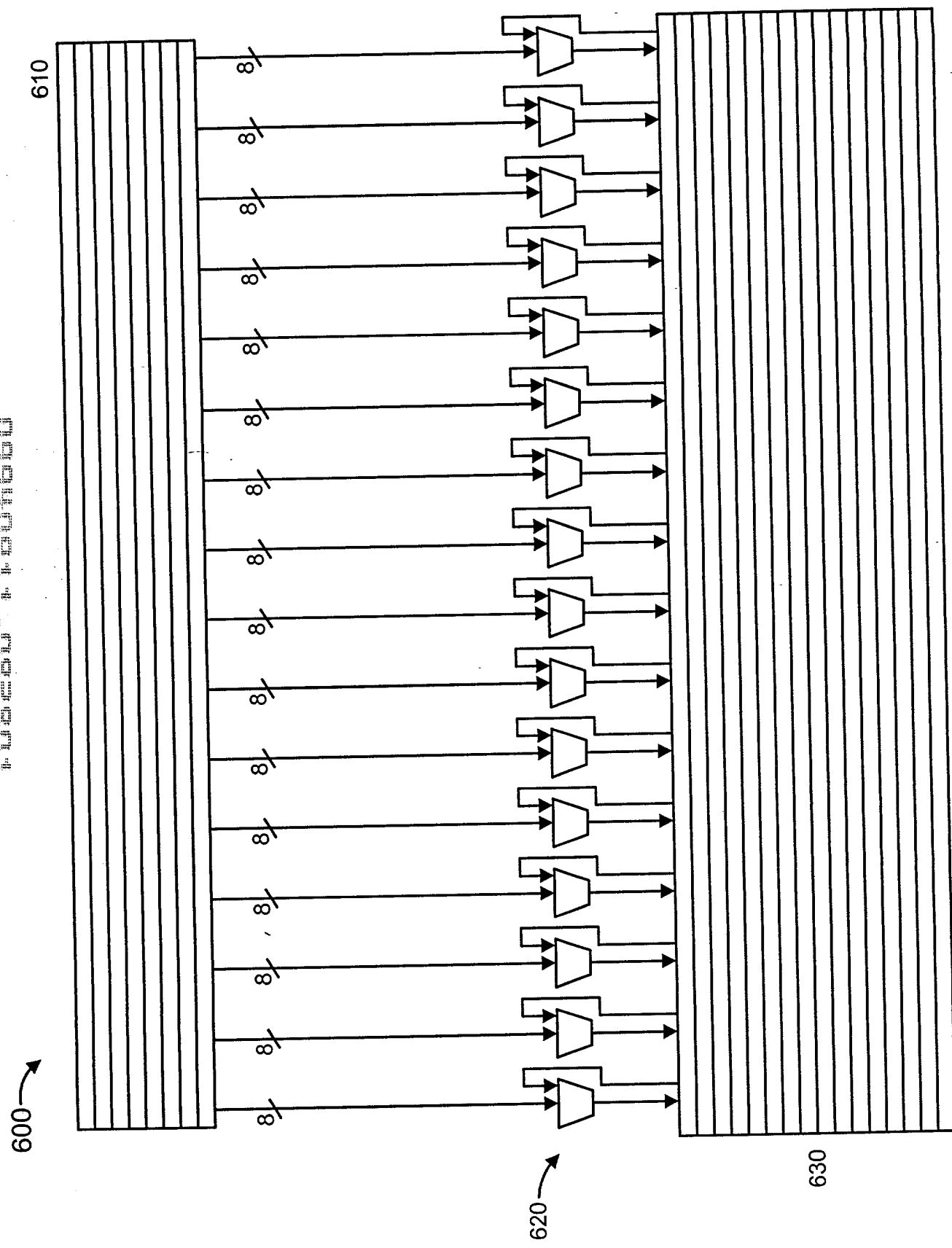


FIG. 6



subfi r3, r5, 3;	compute rotate count	$r3 = 3 - r5$
rotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
stb r2, 0(r5)	; store byte	mem[r5] = r2 using a 1-byte mask aligned with address 0(r5)

FIG. 7

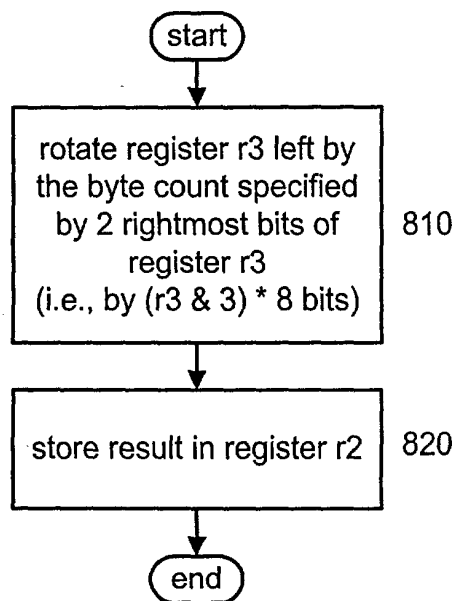


FIG. 8  
(PRIOR ART)

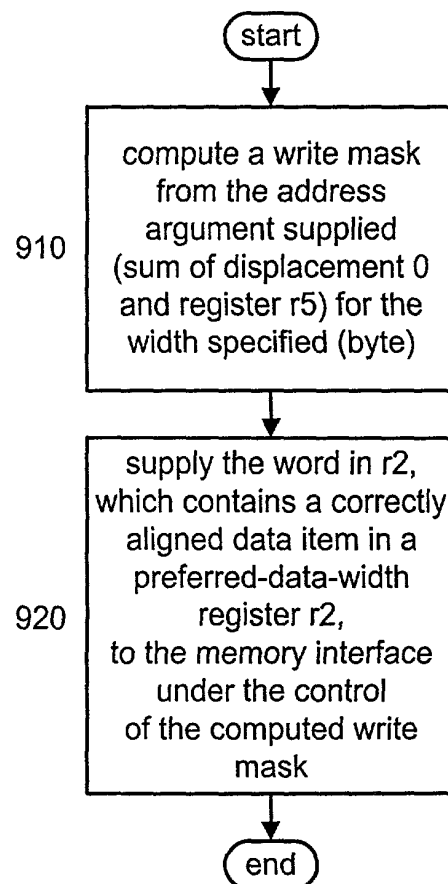


FIG. 9



818  
 YDR9-2001-0606 (8728-545)

subfi r3, r5, 12	; compute rotate count	$r3 = 12 - r5$
vrotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
vstw r2, 0(r5)	; store word	mem[r5] = r2 using a 1-word (4-byte) mask aligned with address 0(r5)

FIG. 10

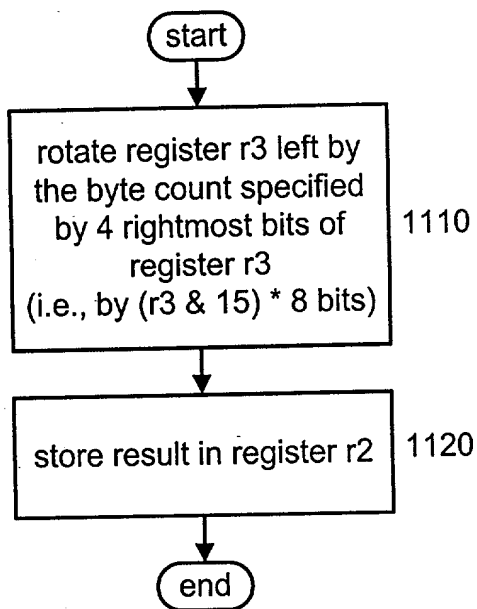


FIG. 11  
 (PRIOR ART)

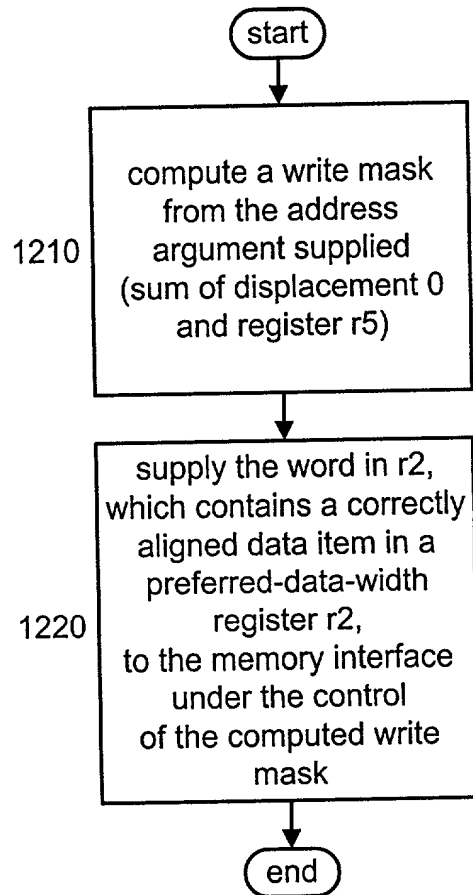


FIG. 12